

- 1pps input and output for timing synchronization
- ToD (Time of Day)
- RS232 digital interface


**GENERAL SPECIFICATION**

Frequency	10.000MHz
Output Waveform	3.3V CMOS
Load Impedance	10MΩ//10pF
Rise/Fall Time	10ns max.
Output Logic 'High'	2.7V min.
Output Logic 'Low'	0.4V max.
Short Term (ADEV)	0.05ppb max. @ Tau = 1sec
	0.016ppb max. @ Tau = 10sec
	0.005ppb max. @ Tau = 100sec
Duty Cycle	50±10%
Frequency Stability Over Temperature	±0.5ppb (-20°C to 60°C, Temperature Slope <0.5°C/min)
Frequency Accuracy	±0.05ppb max.
Daily Aging	±0.005ppb max.
Frequency Control (Resolution = 1x10 <sup>-12</sup> )	±1ppb min.

**1pps Time Output**

1pps	1Hz
Output Amplitude	3.3V CMOS
Pulse Width	1us min.
Rise/Fall Time	10ns max.
Load	10MΩ//10pF

**Built-In Test Equipment (BITE) Output**

Format	3.3V CMOS
Load Impedance	1MΩ
Logic	0 = Normal Operation 1 = Alarm

**Supply Voltage**

Supply Voltage	5VDC typ.
Steady Power	6W max.
Warm-up Power	20W max.
Warm-up Time	7 min. max.

**Digital Communication**

Storage Temperature	-55°C to +85°C, non-operating
Magnetic Sensitivity	<2 Gauss

**1pps Time Input**

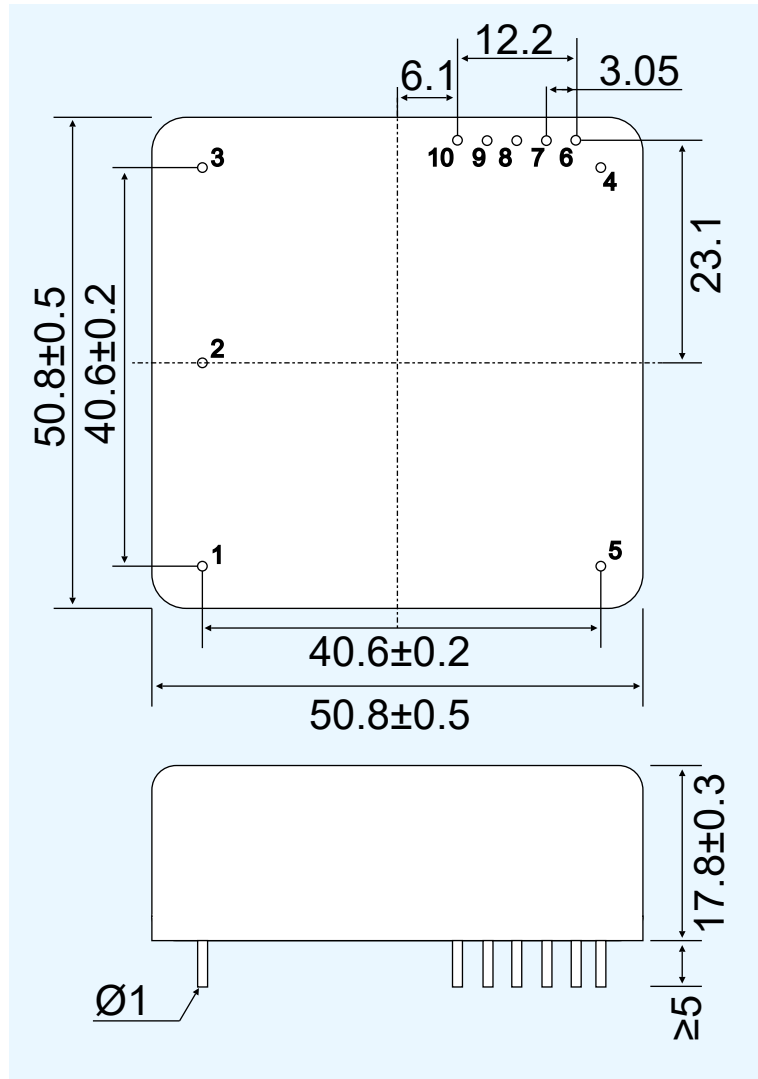
1pps	1Hz
Timing Edge	Rising Edge
Low Level	0.5V max.
High Level	2.9±0.4V min.
Input Impedance	10MΩ//10pF

**Phase Noise @10MHz**

Offset	Phase Noise
10Hz	-85dBc/Hz
100Hz	-115dBc/Hz
1kHz	-135dBc/Hz
10kHz	-140dBc/Hz

**Digital Communication**

Protocol	RS232
Logic Level	3.3V CMOS
Baud Rate	57600
Number of Data Bits	8
Number of Stop Bits	1
Parity	None

**Pin Connections**

- Pin 1: No Connection
- Pin 2: Ground
- Pin 3: 10MHz Output
- Pin 4: Ground
- Pin 5: Vcc
- Pin 6: BITE
- Pin 7: TX
- Pin 8: RX
- Pin 9: 1pps in
- Pin 10: 1pps out

**Ordering Part Number****EQXO-DTA320**