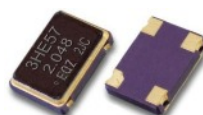


FEATURES

- Femto second integrated phase jitter 150fs typical.
- Superior phase noise performance
- Wide operating temperature from -55° to +125°C



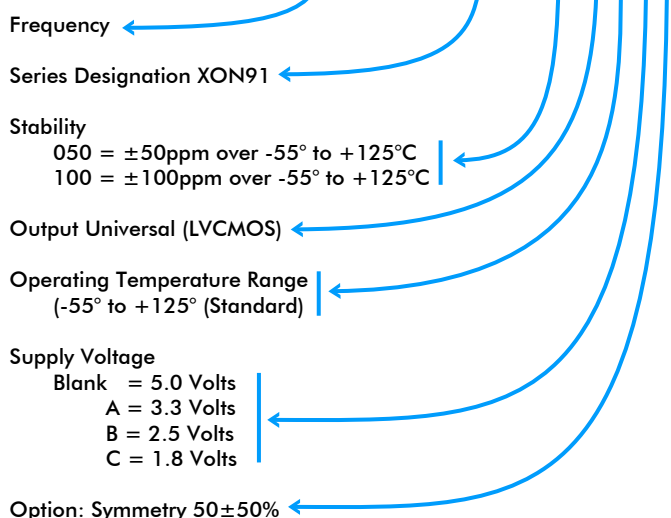
SPECIFICATION

Model Number	XON Series			
Output Logic	LVCMOS			
Supply Voltage V _{DD}	1.8 V _{DD} ±10%	+2.5 V _{DD} ±10%	+3.3 V _{DD} ±10%	+5.0 V _{DD} ±10%
Logic High "1" (90% of V _{DD} minimum)	1.62V	2.25V	2.97V	4.5V
Logic Low "0" (10% of V _{DD} maximum)	0.18V	0.25V	0.33V	0.5V
Current Consumption (max.)	1.75 ~ 20MHz: 2mA	1.75 ~ 20MHz: 3mA	1.75 ~ 20MHz: 4mA	1.75 ~ 20MHz: 5mA
	20 ~ 60MHz: 4mA	20 ~ 60MHz: 5mA	20 ~ 60MHz: 6mA	20 ~ 60MHz: 8mA
Rise Time (Tr) / Fall Time (Tf)	7 ns max.	7ns max.	10ns max.	10ns max.
	Measured between 10% ~ 90% of wave form (CL = 15pF)			
Load	15pF			
Start-up Time	5ms max.			
Duty Cycle	Standard: 50%±10% ; Option 50% ±5% (Add "S" after the part number for this option)			
Tristate Function	Tristate function on Pad 1 is standard for XON53 series oscillators			
Phase Jitter (RMS) [25MHz, 3.3V]	150 fs typical (12kHz to 20MHz integrated)			

SSB Phase Noise [25MHz, 3.3V]	Offset	10Hz	100Hz	1kHz	10kHz	100kHz	1MHz	5MHz
	dBc/Hz (typical)		-94	-127	-142	-156	-161	-163
Storage Temperature	-55° to +125°C							
Ageing at 25°C	±2ppm maximum for first year							
Solder Profile	260°C max.							

ORDERING/PART NUMBER GENERATION

Example: **20.000MHz XON91050UMAS**



OUTLINE & DIMENSIONS

