

CMOS, 6 pad SMD, MHz Range

1.25MHz to 156.0MHz

- Miniature 11.4 x 9.6 x 3.0mm SMD package
- Wide frequency range: 1.25MHz to 156.0MHz
- Supply voltage 2.8, 3.0, 3.3 or 5.0 Volts
- Frequency stability from ± 1 ppm over -30 to +75°C
- **RoHS** compliant

DESCRIPTION

EM63GT series TCXOs are packaged in a miniature 6 pad ceramic SMD package. With squarewave (CMOS) output, tolerances are available from $\pm 1.0 ppm$ over -30° to +75°C. The part has a $0.01 \mu F$ decoupling capacitor built in.

SPECIFICATION

Product Series Code

TCXO: EM63GT

VCTCXO: VEM63GT

1.25MHz to 156.0MHz Frequency Range: Output Waveform: Squarewave, HCMOS **Initial Calibration Tolerance:** $<\pm2.0$ ppm at +25° ±2 °C

Standard Frequencies: 10.0, 12.8, 13.0, 14.4, 15.36, 16.384, 19.2, 19.440, 19.68,

25.0, 20.0, 27.0, 38.880, 40.0, 77.760, 125.0, 155.520

(Partial list) See table

Operating Temperature Range:

Frequency Stability

vs. Ageing: ±1.0 ppm max. first year vs. Voltage Change: ±0.3 ppm max. ±5% change vs. Load Change: ±0.3 ppm max. ±10% change vs. Reflow (SMD type): ±1.0ppm max. for one reflow (Measured after 24 hours)

Mechanical Frequency Tuning: ±3ppm minimum

Supply Voltage: +2.8, +3.0, +3.3 or +5.0V

(See table)

Output Logic Levels: Logic High: 90% Vdd min. Logic Low: 10% Vdd max.

Rise and Fall Times: 10ns max.

 $50\% \pm 10\%$ standard. **Duty Cycle:**

50%±5% option

5ms typical, 10ms max. Start-up Time:

Current Consumption: See table below

Output Load: 15pF

-55~+125°C Storage Temperature:

FREQUENCY STABILITY

Frequency Stability (ppm)		±0.5	±1.0	±1.5	±2.0	±2.5
Temperature Range (°C)	0 ~ +50	✓	✓	✓	✓	✓
	-10 ~ +60	ASK	✓	✓	✓	✓
	-20 ~ + 7 0	х	✓	✓	✓	✓
	-30 ~ +75	х	✓	✓	✓	✓
	-40 ~ +85	х	✓	✓	✓	✓

√ = available, x = not available, ASK = call Technical Sales

VEM63GT VOLTAGE CONTROL SPECIFICATION

Control Voltage: Standard = $+1.5\pm1.0$ Volts for all input

voltages. (Contact technical sales if

+2.5±2.0 Volts is required.)

 ± 6.0 ppm min. (Vcon = +4.5V ± 1.0 V) Frequency Deviation: Positive (increase of control voltage Slope Polarity:

increases

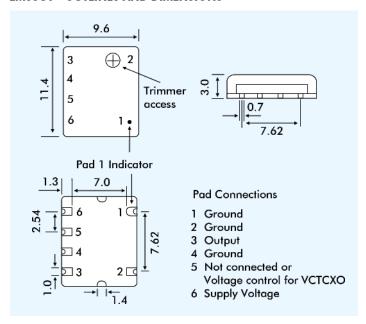
output frequency.)

50kΩ minimum Input Impedance: Modulation Bandwidth: 20kHz minimum Linearity: ±10% maximum





EM63GT - OUTLINES AND DIMENSIONS



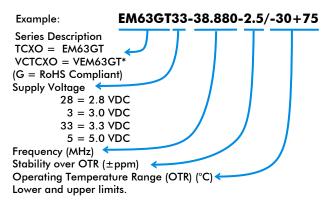
INPUT VOLTAGE & CURRENT CONSUMPTION

Input Voltage/ Frequency	+2.8V	+3.0	+3.3V	+5.0 V	
8.192MHz	2mA	2mA		5mA	
10.0MHz	3mA	4mA		7mA	
77.760MHz	14mA	17mA		32mA	
155.520MHz	26mA	35mA		50mA	

SSB PHASE NOISE at 25°C

Offset		10Hz	100Hz	1kHz	10kHz	100kHz
Part = M62GT33	at 10.0Mhz (dBc/Hz)	-115	-135	-148	-152	-155
	at 155.250Mhz (dBc/Hz)	-72	-110	-125	-132	-125

PART NUMBERING PROCEDURE



* Above 40MHz part numbers are EML63GT or VEML63GT to indicate use of a Phase Locked Loop (PLL) circuit above this frequency.